

TILAK MAHARASHTRA VIDYAPEETH, PUNE
MASTER OF COMPUTER APPLICATIONS
(Specialization in Cloud Technology)
EXAMINATION :MAY - 2024
SEMESTER - I
Sub: Computer Architecture (MCCL23-101)

Date :22/05/2024

Total Marks :60

Time: 2.00 pm to 4.30 pm

Instruction:

1. All questions are compulsory unless and otherwise stated.
 2. Bold figures to the right of every question are the maximum marks for that question.
 3. Candidates are advised to attempt questions in order.
 4. Answers written illegibly are likely to be marked zero.
 5. Use of scientific calculators, Log tables, Mollier Charts is allowed.
 6. Draw neat and labelled diagram wherever necessary.
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Q.1. Answer the following in 2-3lines. (Any 5) (10)

1. Define the term cache memory.
2. Convert $(1101.111)_2 = (?)_{10}$.
3. Define the term register transfer.
4. Write a short note on memory reference instructions.
5. Define the term associative memory.
6. What is meant by priority interrupt?
7. Define the term auxiliary memory.

Q. 2. Answer the following in short. (Any 4) (20)

1. Describe the various types of computer.
2. Write a short note on register transfer language (RTL).
3. Draw and explain RISC architecture with its characteristics.
4. What are the various secondary storage devices?
5. Draw and explain the concept of input/output interface.
6. What are the various modes of transfer data? Explain.

Q. 3. Answer the following in detail. (Any 3) (30)

1. Describe any five addressing modes with example.
2. What are the major categories of control unit? Explain with suitable diagrams.
3. Draw and explain the various functional units of a computer.
4. State the difference between software polling and daisy chaining.
5. What are the various categories of peripheral devices? Explain in detail.
